Digital Logic Circuit Analysis And Design

Digital Logic Circuit Analysis and DesignDigital Circuit Analysis and Design with Simulink Modeling and Introduction to CPLDs and FPGAsDigital Logic Circuit Analysis and DesignDigital Logic Circuit Analysis and DesignIntroduction to Electrical Circuit AnalysisEssential Circuit Analysis using LTspice®Digital Logic Circuit Analysis and Design (second Edition)Digital Logic Circuit Analysis and Design [rental Edition]Fluid Power Logic Circuit DesignGeneral Support Maintenance ManualDigital Logic and Switching CircuitsDesign, Analysis and Test of Logic Circuits Under UncertaintyLogic circuit analysis program (lcap)Computer Circuit AnalysisDigital Logic CircuitsLogic Circuit DesignDigital CircuitsDigital Circuit Design with an Introduction to CPLDs and FPGAsComputer-aided Circuit Analysis Using SPICEDesign of Digital Systems Victor Peter Nelson Steven T. Karris Victor Peter Nelson Victor Peter Nelson Ozgur Ergul Farzin Asadi Victor Peter Nelson Victor P Nelson Peter Rohner Jefferson C. Boyce Smita Krishnaswamy S. Alpert Frank A. Ilardi Robert Gordon Middleton Shimon P. Vingron William J. Streib Steven T. Karris Walter Banzhaf Gregory L. Moss

Digital Logic Circuit Analysis and Design Digital Circuit Analysis and Design with Simulink Modeling and Introduction to CPLDs and FPGAs Digital Logic Circuit Analysis and Design Digital Logic Circuit Analysis and Design Introduction to Electrical Circuit Analysis Essential Circuit Analysis using LTspice® Digital Logic Circuit Analysis and Design (second Edition) Digital Logic Circuit Analysis and Design [rental Edition] Fluid Power Logic Circuit Design General Support Maintenance Manual Digital Logic and Switching Circuits Design, Analysis and Test of Logic Circuits Under Uncertainty Logic circuit analysis program (lcap) Computer Circuit Analysis Digital Logic Circuits Logic Circuit Design Digital Circuits Digital Circuit Design with an Introduction to CPLDs and FPGAs Computer-aided Circuit Analysis Using SPICE Design of Digital Systems *Victor Peter Nelson Steven T. Karris Victor Peter Nelson Victor Peter Nelson Ozgur Ergul Farzin Asadi Victor Peter Nelson Victor P Nelson Peter Rohner Jefferson C. Boyce Smita Krishnaswamy S. Alpert Frank A. Ilardi Robert Gordon Middleton Shimon P. Vingron William J. Streib Steven T. Karris Walter Banzhaf Gregory L. Moss*

this book is an undergraduate level textbook presenting a thorough discussion of state of the art digital devices and circuits it is self contained

for introductory digital logic design or computer engineering courses in electrical and computer engineering or computer science at the sophomore or junior level many recent texts place instructors in the difficult position of choosing between authoritative state of the art coverage and an approach that is highly supportive of student learning this carefully developed text was widely praised by reviewers for both its great clarity and its rigor the book balances theory and practice in depth without getting bogged down in excessive technical or mathematical language and has abundant coverage of current topics of interest such as programmable devices computer aided design and testability an unusually large number of illustrations examples and problems help students gain a solid sense of how theory underlies practice

the advent in the 1980s of low cost easy to use computers such as the ibm personal computer and the apple ii led to decades of expanding applications of computers in all aspects of life later the internet made it feasible to interconnect computers around the world which spurred even more uses of computers including cloud computing the continued miniaturization and cost reduction of microelectronics has resulted in the prolifirization of mobile devices an emergence of the internet of things iot and the rise of on chip parallel processing continued evolution of computer hardware coupled with advances in artificial intelligence and software will lead to even more sophisticated applications in the years to come

a concise and original presentation of the fundamentals for new to the subject electrical engineers this book has been written for students on electrical engineering courses who don t necessarily possess prior knowledge of electrical circuits based on the author s own teaching experience it covers the analysis of simple electrical circuits consisting of a few essential components using fundamental and well known methods and techniques although the above content has been included in other circuit analysis books this one aims at teaching young engineers not only from electrical and electronics engineering but also from other areas such as mechanical engineering aerospace engineering mining engineering and chemical engineering with unique pedagogical features such as a puzzle like approach and negative case examples such as the unique when things go wrong section at the end of each chapter believing that the traditional texts in this area can be overwhelming for beginners the author approaches his subject by providing numerous examples for the student to solve and practice before learning more complicated components and circuits these exercises and problems will provide instructors with in class activities and tutorials thus establishing this book as the perfect complement to the more traditional texts all examples and problems contain detailed analysis of various circuits and are solved using a recipe approach

providing a code that motivates students to decode and apply to real life engineering scenarios covers the basic topics of resistors voltage and current sources capacitors and inductors ohm s and kirchhoff s laws nodal and mesh analysis black box approach and thevenin norton equivalent circuits for both dc and ac cases in transient and steady states aims to stimulate interest and discussion in the basics before moving on to more modern circuits with higher level components includes more than 130 solved examples and 120 detailed exercises with supplementary solutions accompanying website to provide supplementary materials wiley com go ergul4412

this textbook provides a compact but comprehensive treatment that guides students through the analysis of circuits using ltspice ideal as a hands on source for courses in circuits electronics digital logic and power electronics this text focuses on solving problems using market standard software corresponding to all key concepts covered in the classroom the author uses his extensive classroom experience to guide students toward deeper understanding of key concepts while they gain facility with software they will need to master for later studies and practical use in their engineering careers

本书以介绍数字设计的基础知识以及丰富案例为主要特色 并在第一版的基础上进行了全面的修订与更新 更加突出了数字设计相关技术的应用 本书内容包括 计算机与数字系统 数制系统 逻辑电路与布尔代数 组合逻辑电路分析与设计 时序逻辑电路简介 同步时序逻辑电路分析与设计 异步时序逻辑电路分析与设计 可编程逻辑器件 数字系统设计

this print textbook is available for students to rent for their classes the pearson print rental program provides students with affordable access to learning materials so they come to class ready to succeed balance breadth and depth of coverage with practical real world design methods digital logic circuit analysis and design provides an authoritative state of the art approach to the fundamentals of digital logic analysis and design that is highly supportive of student learning the book balances theory and practice in depth without getting bogged down in excessive technical or mathematical language retaining its tradition of both clarity and rigor the 2nd edition features extensive coverage of current topics of interest such as modeling with verilog and vhdl design with programmable devices and computer aided design filled with updated illustrations examples and problems this text helps students gain a solid sense of how theory underlies practice this title is also available digitally as a standalone pearson etext contact your pearson rep for more information

logic circuits are becoming increasingly susceptible to probabilistic behavior caused by external radiation and process variation in addition inherently probabilistic quantum and nano technologies are on the horizon as we approach the limits of cmos scaling ensuring the reliability of such circuits despite the probabilistic behavior is a key challenge in ic design one that

necessitates a fundamental probabilistic reformulation of synthesis and testing techniques this monograph will present techniques for analyzing designing and testing logic circuits with probabilistic behavior

in three main divisions the book covers combinational circuits latches and asynchronous sequential circuits combinational circuits have no memorising ability while sequential circuits have such an ability to various degrees latches are the simplest sequential circuits ones with the shortest memory the presentation is decidedly non standard the design of combinational circuits is discussed in an orthodox manner using normal forms and in an unorthodox manner using set theoretical evaluation formulas relying heavily on karnaugh maps the latter approach allows for a new design technique called composition latches are covered very extensively their memory functions are expressed mathematically in a time independent manner allowing the use of normal non temporal boolean logic in their calculation the theory of latches is then used as the basis for calculating asynchronous circuits asynchronous circuits are specified in a tree representation each internal node of the tree representing an internal latch of the circuit the latches specified by the tree itself the tree specification allows solutions of formidable problems such as algorithmic state assignment finding equivalent states non recursively and verifying asynchronous circuits

partial contents transistor theory mosfets logic element input and output logic circuit design karnaugh maps roms rams magnetic memories proms eproms and eeproms digital signal voltage levels and more this is intended as an introductory text for courses in computer design circuit theory troubleshooting and servicing all of the basic theory that is needed is developed in the text 640 illustrations including diagrams and charts index

this book is an undergraduate level textbook presenting a thorough discussion of state of the art digital devices and circuits it supplements our electronic devices and amplifier circuits isbn 0 9744239 4 7 it is self contained begins with the basics and ends with the latest developments of the digital technology the intent is to prepare the reader for advanced digital circuit design and programming the powerful complex programmable logic devices cplds and field programmable gate arrays fpgas the prerequisites for this text are just basic high school math accordingly it can be read and understood by high school seniors trade school community college and 4 year university students it is ideal for self study chapter 1 is an introduction to the decimal binary octal and hexadecimal numbers their representation and conversion from one base to another chapter 2 presents an introduction to arithmetic operations in binary octal and hexadecimal numbers the tens complement and nines complements in the decimal system and the twos complement and ones complements in the

binary system are discussed and illustrated with numerous examples chapter 3 begins with an introduction to sign magnitude representation of binary numbers it concludes with a discussion on floating point arithmetic for representing large numbers and the ieee standard that specifies single precision 32 bit and double precision 64 bit floating point representation of numbers chapter 4 describes the most commonly used binary codes the binary coded decimal bcd the excess 3 code the 2 421 code the gray code and the american standard code for information interchange ascii code are introduced as well as the use of parity bits chapter 5 begins with the basic logic operations and continues with the fundamentals of boolean algebra and the basic postulates and theorems as applied to electronic logic circuits truth tables are defined and examples are given to illustrate how they can be used to prove boolean algebra theorems or equivalent logical expressions chapter 6 introduces the standard forms of expressing boolean functions the minterms and maxterms also known as standard products and standard sums respectively a procedure is also presented to show how one can convert one form to the other this topic is essential in understanding the programming of programmable logic arrays plas discussed in chapter 11 chapter 7 is an introduction to combinational logic circuits it begins with methods of implementing logic diagrams from boolean expressions the derivation of boolean expressions from logic diagrams input and output waveforms and the use of karnaugh maps for simplifying boolean expressions chapter 8 is an introduction to sequential logic circuits it begins with a discussion of the different types of flip flops and continues with the analysis and design of binary counters registers ring counters and ring oscillators chapter is an introduction to computer memory devices we discuss the random access memory ram read only memory rom row and column decoders memory chip organization static rams srams dynamic rams drams volatile nonvolatile programmable roms proms erasable proms eproms electrically erasable proms eeproms flash memories and cache memory chapter 10 begins with an introduction to the basic components of a digital computer it continues with a discussion of the basic microprocessor operations and concludes with the description of more advanced arithmetic and logic operations we consider chapter 11 as the highlight of this text it is an introduction to field programmable devices fpds also referred to as programmable logic devices plds it begins with the description and applications of programmable logic arrays plas continues with the description of simple plds splds and complex plds cplds and concludes with the description of field programmable gate arrays fpgas this text includes also four appendices appendix a is an overview of the advanced boolean equation language abel which is an industry standard hardware description language hdl used in programmable logic devices plds appendix b describes the vhsic hardware description language briefly referred to as vhdl this language was

developed to be used for documentation verification and synthesis of large digital designs appendix c introduces the verilog hardware description language hdl like vhdl introduced in appendix b verilog is a programming language used to describe a digital system and its components appendix d is a brief discussion on the boundary scan architecture and the new technology trends that make using boundary scan essential for the reduction in development and production costs

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